

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Toshihiro TANAKA et al.

Appln. No.: Group Art Unit:

Filed: HEREWITH

For: NONVOLATILE MEMORY DEVICE AND SEMICONDUCTOR DEVICE

* * *

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

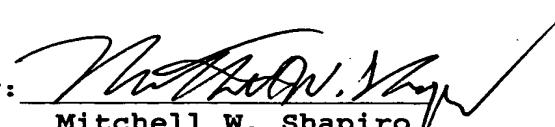
Pursuant to 37 C.F.R. § 1.56, and without any assertion as to materiality or prior art effect, the documents listed on the attached Form PTO-1449 are hereby cited.

The Japanese documents on the attached List were cited in the specification, on page 3, and their relevance is indicated therein. Document AA is the U.S. counterpart of Document AJ. Document AB is the U.S. counterpart of Document AK.

Respectfully submitted,

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March 22, 2004

FORM PTO-1449			Atty. Docket No. XA-10057	Appln. No.			
<u>LIST OF DOCUMENTS CITED BY APPLICANT</u>			Applicant Toshihiro TANAKA et al.				
			Filing Date HEREWITH		Group		
U. S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Sub-class	Filing Date
	AA	6,388,293 B1	05/14/2002	Ogura et al.	257	365	
	AB	6,567,315 B2	05/20/2003	Takase et al.	365	185.28	
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
FOREIGN PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Country	Class	Sub-class	Translation
	AJ	2001-148434	05/29/2001	Japan			Abstract
	AK	2002-197876	07/12/2002	Japan			Abstract
	AL						
	AM						
	AN						
	AO						
OTHER (including author, title, date, pertinent pages, etc.)							
	AP						
	AQ						
	AR						
Examiner			Date Considered				
XAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.							